

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Naoki YAMAMOTO  
Serial No.: Not yet assigned  
Divisional Application of Serial No. 09/639,306, Issued as  
Patent No. 6,645,799  
Filed: Even date herewith  
For: **METHOD OF MANUFACTURING A DUAL GATE  
SEMICONDUCTOR DEVICE WITH A POLY-METAL  
ELECTRODE (As Amended)**  
Group: 2813 (expected)  
Examiner: Jack S. J. Chen (expected)

**CLAIM FOR PRIORITY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

February 27, 2004

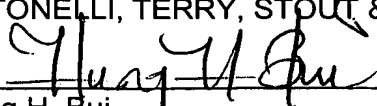
Sir:

Pursuant to 35 U.S.C. §119 and 37 CFR 1.55, the applicants hereby claim the  
right of priority based on the following foreign applications filed in Japan:

Application No. 2000-017875 filed on January 24, 2000.

A certified copy of this foreign application was filed on September 27, 2000, in  
application Serial No. 09/639,306, the parent application of the present divisional  
application.

Respectfully submitted,  
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